

## APPLICATION DATA SHEET

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<b>Title of Invention</b>	<b>AN INTEGRATED CIRCUIT WITH DIELECTRIC DIFFUSION BARRIER LAYER FORMED BETWEEN INTERCONNECTS AND INTERLAYER DIELECTRIC LAYERS</b>
<b>Application Type:</b> regular, utility <b>Attorney Docket Number:</b> F0361.C1.D1 <b>Request Not To Publish</b>  I/We hereby request that the attached application not be published under 37 U.S.C. 122(b).  I/We hereby certify that the invention disclosed in the attached application has not and will not be the subject of an application filed in another country, or under a multilateral agreement, that requires publication at eighteen months after filing.	
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<b>Continuing Data:</b>  This is a Division of US application number 10/226,520, filed 2002-08-22 , now co-pending.  US application number 10/226,520, filed 2002-08-22 is a Continuation of US application number 09/874,175, filed 2001-06-04 , now US patent number 6,469,385 B1, issued 2002-10-22.	
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